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PATENT  
DOCKET NO.: 2207/9865  
Assignee: Intel Corporation

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

APPLICANTS : Manoj Khare et al.  
SERIAL NO. : 09/749,660  
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FOR : METHOD AND APPARATUS FOR REDUCING  
MEMORY LATENCY IN A CACHE COHERENT  
MULTI-NODE ARCHITECTURE  
GROUP ART UNIT : 2186  
EXAMINER : Tuan V. Thai  
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**AMENDMENT**

S I R:

The following amendments and remarks are submitted in response to the Office  
Action dated August 28, 2006.

Amendments to the claims begin on page 2.

Remarks begin on page 8.